

# ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

## Title of Invention

METHOD OF FABRICATING A CMOS DEVICE WITH DUAL METAL GATE ELECTRODES

Submission Type : Information Disclosure Statement

Application Number:

10/826665



EFS ID:

70530

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First Named Applicant: CHANG PARK

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-ids	061472-0308425-usldst.xml	1201	2004-10-15
us-ids	us-ids.dtd	7763	2004-10-15
us-ids	us-ids.xsl	12026	2004-10-15
package-data	061472-0308425-pkda.xml	1779	2004-10-15
package-data	package-data.dtd	27025	2004-10-15
package-data	us-package-data.xsl	19263	2004-10-15
Total files size		69057	

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